

Title: Inverter 74ls48

Generated on: 2026-04-24 14:48:30

Copyright (C) 2026 JAC-INVERT. All rights reserved.

For the latest updates and more information, visit our website: <https://jackedup.co.za>

-----

Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. NOTES:  
1. Voltage values are with respect to network ground terminal. 2. The package thermal ...

The SN54/74LS48 is a BCD to 7-Segment Decoder consisting of NAND gates, input buffers and seven AND-OR-INVERT gates. Seven NAND gates and one driver are connected in pairs to make BCD ...

Download the 74LS48 datasheet PDF (83.24 KB) by Hitachi Semiconductor (now Renesas). Includes overview, features, pinout, and specifications.

It is used to convert binary coded decimal input into the control signals needed to drive a typical 7-segment LED display, which can be useful in creating digital ...

DM74LS48 BCD to 7-Segment Decoder. General Description. The "LS48 translates four lines of BCD (8421) input data into the 7-segment numeral code and provides seven corresponding outputs ...

Hitachi makes every attempt to ensure that its products are of high quality and reliability.

Part #: 74LS48. Description: BCD-to-Seven-Segment Decoder Driver (Internal Pull-up outputs). File Size: 83.24 Kbytes. Manufacturer: Hitachi Semiconductor.

Description: BCD-to-Seven-Segment Decoder Driver (Internal Pull-up outputs). Manufacturer: Hitachi Semiconductor.

Web: <https://jackedup.co.za>

